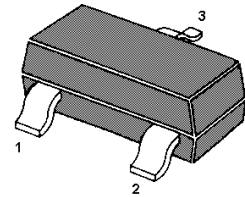
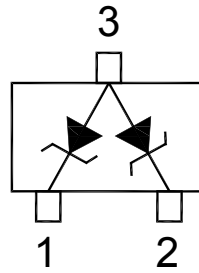


E5V0VUKA2C-CH

ESD Protection Diode

Features

- AEC-Q101 Qualified
- Low capacitance
- Halogen and Antimony Free(HAF),
RoHS compliant



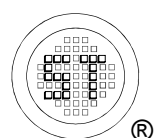
1. Cathode 2. Cathode 3. Anode
SOT-23 Plastic Package

Absolute Maximum Ratings (at $T_a = 25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power ($t_p = 8/20 \mu\text{s}$)	P_{PK}	75	W
Peak Pulse Current ($t_p = 8/20 \mu\text{s}$)	I_{PP}	5	A
ESD (IEC61000-4-2) Air Contact	V_{ESD}	± 15 ± 8	KV
Operating Junction Temperature Range	T_j	- 55 to + 125	$^{\circ}\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^{\circ}\text{C}$

Characteristics at $T_a = 25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Max.	Unit
Reverse Stand-Off Voltage	V_{RWM}	-	5	V
Reverse Breakdown Voltage at $I_R = 1 \text{ mA}$	$V_{(BR)R}$	6.5	9	V
Reverse Current at $V_{RWM} = 5 \text{ V}$	I_R	-	200	nA
Clamping Voltage at $I_{PP} = 5 \text{ A}$, $t_p = 8/20 \mu\text{s}$	V_C	-	15	V
Junction Capacitance at $V_R = 0 \text{ V}$, $f = 1 \text{ MHz}$	C_j	-	0.8	pF



Electrical Characteristic Curves

Fig 1. Pulse Waveform

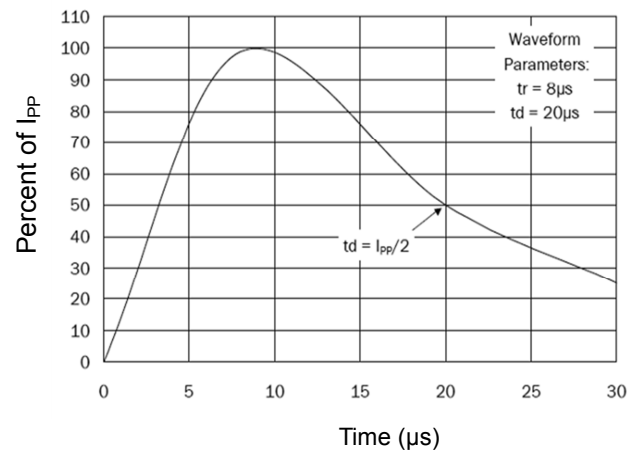


Fig 2. Power Derating Curve

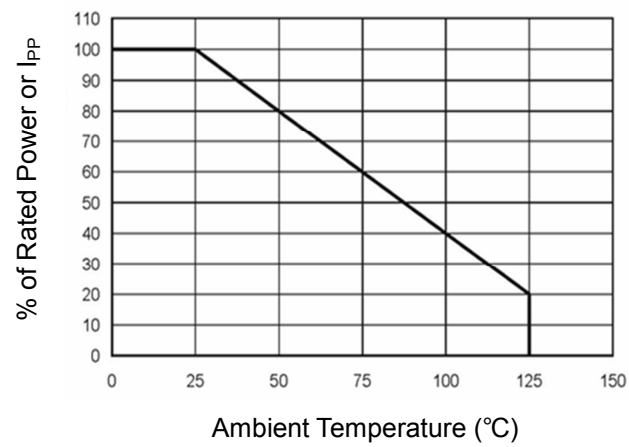


Fig 3. Clamping Voltage Curve

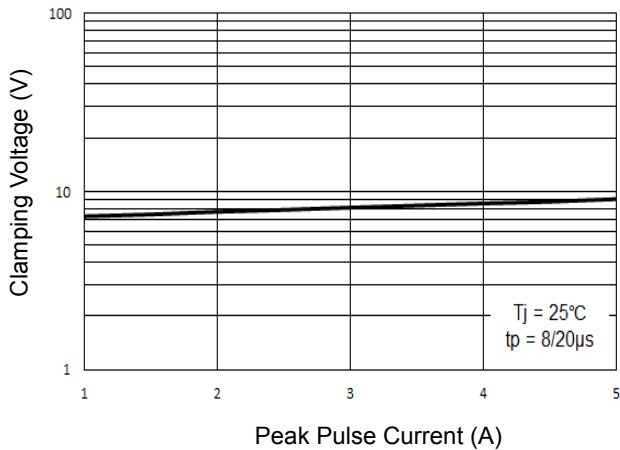


Fig 4. Junction Capacitance

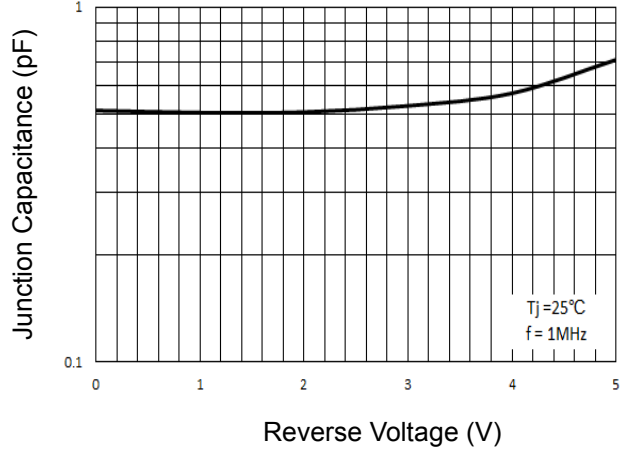
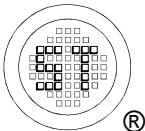
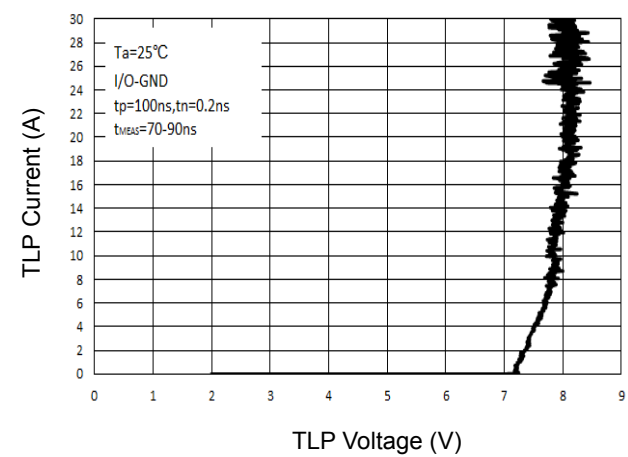


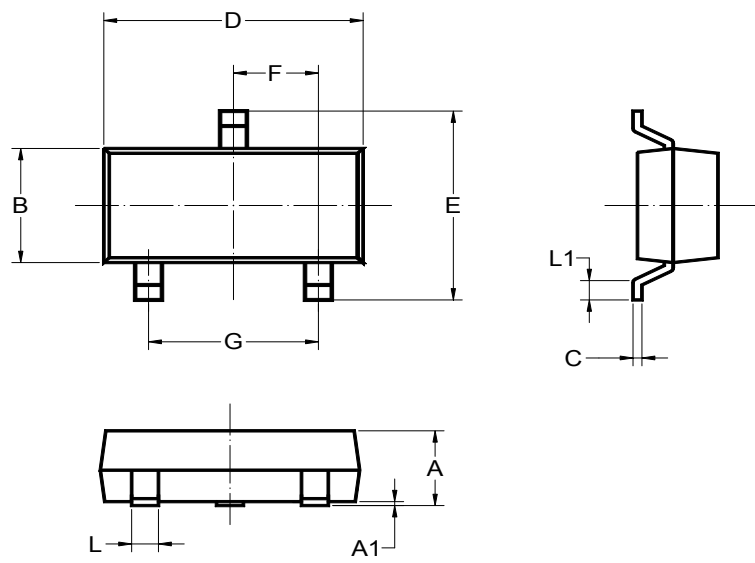
Fig. 5 TLP Curve



E5V0VUKA2C-CH

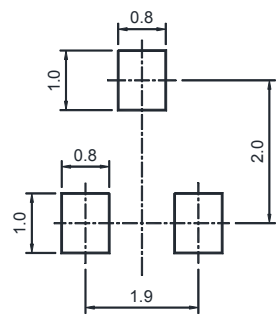
Package Outline (Dimensions in mm)

SOT-23



Unit	A	A1	B	C	D	E	F	G	L	L1
mm	1.20 0.89	0.100 0.013	1.40 1.20	0.19 0.08	3.04 2.80	2.6 2.2	1.02 0.89	2.04 1.78	0.51 0.37	0.2 MIN

Recommended Soldering Footprint



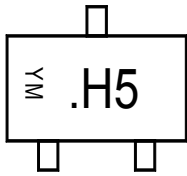
Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

- " H5 " = Part No.
- " . " = HAF (Halogen and Antimony Free)
- " YM " = Date Code Marking
- " Y " = Year
- " M " = Month

Font type: Arial



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