

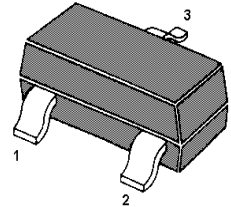
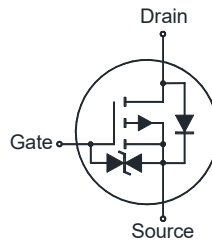
MKA06P260LK-CH

P-Channel Enhancement Mode MOSFET

Features

- AEC-Q101 Qualified
- Surface-mounted package
- Built-in G-S Protection Diode
- Halogen and Antimony Free(HAF),
RoHS compliant
- Typical ESD Protection HBM Class 2

Classification	Voltage Range(V)
0A	< 125
0B	125 to < 250
1A	250 to < 500
1B	500 to < 1000
1C	1000 to < 2000
2	2000 to < 4000
3A	4000 to < 8000
3B	≥ 8000



1. Gate 2. Source 3. Drain
SOT-23 Plastic Package

Applications

- Portable appliances
- Battery management

Absolute Maximum Ratings(at $T_a = 25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$-I_D$	2	A
Pulsed Drain Current ¹⁾	$-I_{DM}$	8	A
Total Power Dissipation ²⁾	P_{tot}	540	mW
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient ²⁾	$R_{\theta JA}$	231	$^{\circ}\text{C/W}$

¹⁾ Pulse Test: Pulse Width $\leq 100 \mu\text{s}$, Duty Cycle $\leq 2\%$, Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^{\circ}\text{C}$.

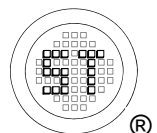
²⁾ Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate in still air.



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Characteristics at $T_a = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $-I_D = 250\ \mu\text{A}$	$-V_{(BR)DSS}$	60	-	-	V
Drain-Source Leakage Current at $-V_{DS} = 60\ \text{V}$	$-I_{DSS}$	-	-	1	μA
Gate-Source Leakage at $V_{GS} = \pm 20\ \text{V}$	I_{GSS}	-	-	± 10	μA
Gate-Source Threshold Voltage at $V_{DS} = V_{GS}$, $-I_D = 250\ \mu\text{A}$	$-V_{GS(th)}$	1	-	3	V
Drain-Source On-State Resistance at $-V_{GS} = 10\ \text{V}$, $-I_D = 2\ \text{A}$ at $-V_{GS} = 4.5\ \text{V}$, $-I_D = 2\ \text{A}$	$R_{DS(on)}$	- -	- -	190 260	m Ω
DYNAMIC PARAMETERS					
Forward Transconductance at $-V_{DS} = 5\ \text{V}$, $-I_D = 2\ \text{A}$	g_{fs}	-	6	-	S
Gate resistance at $V_{GS} = 0\ \text{V}$, $V_{DS} = 0\ \text{V}$, $f = 1\ \text{MHz}$	R_g	-	9.2	-	Ω
Input Capacitance at $V_{GS} = 0\ \text{V}$, $-V_{DS} = 30\ \text{V}$, $f = 1\ \text{MHz}$	C_{iss}	-	361	-	pF
Output Capacitance at $V_{GS} = 0\ \text{V}$, $-V_{DS} = 30\ \text{V}$, $f = 1\ \text{MHz}$	C_{oss}	-	25	-	pF
Reverse Transfer Capacitance at $V_{GS} = 0\ \text{V}$, $-V_{DS} = 30\ \text{V}$, $f = 1\ \text{MHz}$	C_{rss}	-	20	-	pF
Total Gate Charge at $-V_{GS} = 10\ \text{V}$, $-V_{DS} = 30\ \text{V}$, $-I_D = 2\ \text{A}$ at $-V_{GS} = 4.5\ \text{V}$, $-V_{DS} = 30\ \text{V}$, $-I_D = 2\ \text{A}$	Q_g	- -	7 3.2	- -	nC
Gate-Source Charge at $-V_{GS} = 10\ \text{V}$, $-V_{DS} = 30\ \text{V}$, $-I_D = 2\ \text{A}$	Q_{gs}	-	1.6	-	nC
Gate-Drain Charge at $-V_{GS} = 10\ \text{V}$, $-V_{DS} = 30\ \text{V}$, $-I_D = 2\ \text{A}$	Q_{gd}	-	1.3	-	nC
Turn-On Delay Time at $-V_{GS} = 10\ \text{V}$, $-V_{DD} = 30\ \text{V}$, $-I_D = 2\ \text{A}$, $R_G = 3.3\ \Omega$	$t_{d(on)}$	-	5.7	-	ns
Turn-On Rise Time at $-V_{GS} = 10\ \text{V}$, $-V_{DD} = 30\ \text{V}$, $-I_D = 2\ \text{A}$, $R_G = 3.3\ \Omega$	t_r	-	3.4	-	ns
Turn-Off Delay Time at $-V_{GS} = 10\ \text{V}$, $-V_{DD} = 30\ \text{V}$, $-I_D = 2\ \text{A}$, $R_G = 3.3\ \Omega$	$t_{d(off)}$	-	7.5	-	ns
Turn-Off Fall Time at $-V_{GS} = 10\ \text{V}$, $-V_{DD} = 30\ \text{V}$, $-I_D = 2\ \text{A}$, $R_G = 3.3\ \Omega$	t_f	-	1.8	-	ns
Body-Diode PARAMETERS					
Body Diode Voltage at $-I_S = 1\ \text{A}$, $V_{GS} = 0\ \text{V}$	$-V_{SD}$	-	-	1.2	V
Body-Diode Continuous Current	$-I_S$	-	-	2	A
Body Diode Reverse Recovery Time at $-I_S = 2\ \text{A}$, $di/dt = 100\ \text{A} / \mu\text{s}$	t_{rr}	-	8	-	ns
Body Diode Reverse Recovery Charge at $-I_S = 2\ \text{A}$, $di/dt = 100\ \text{A} / \mu\text{s}$	Q_{rr}	-	3.6	-	nC



Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

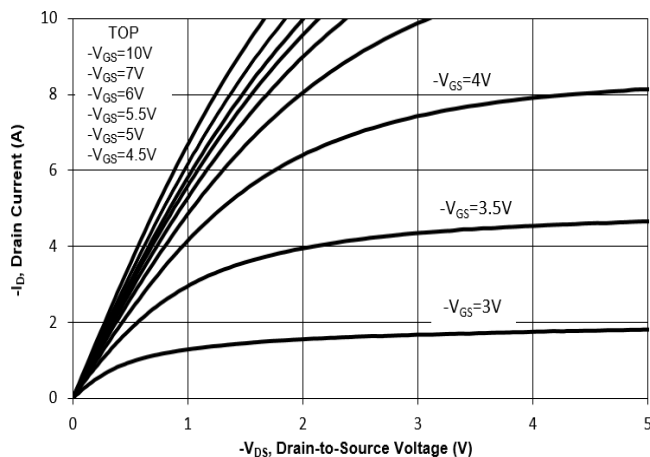


Fig. 2 Typical Transfer Characteristics

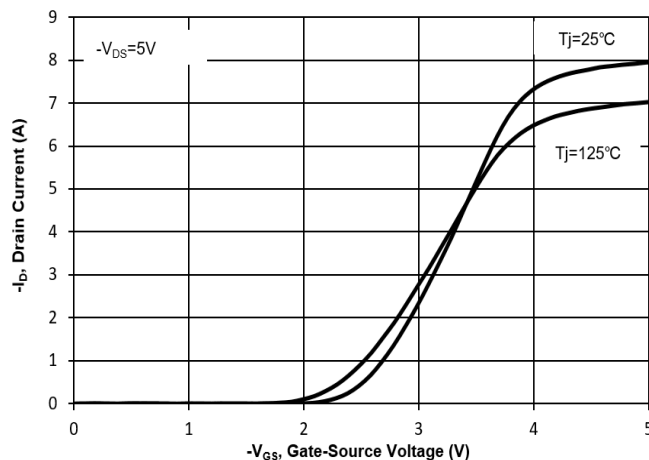


Fig. 3 on-Resistance vs. Drain Current

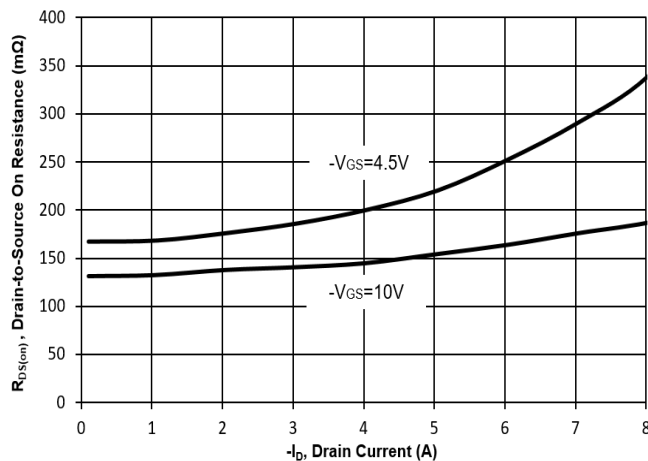


Fig. 4 on-Resistance vs. Gate-to-Source Voltage

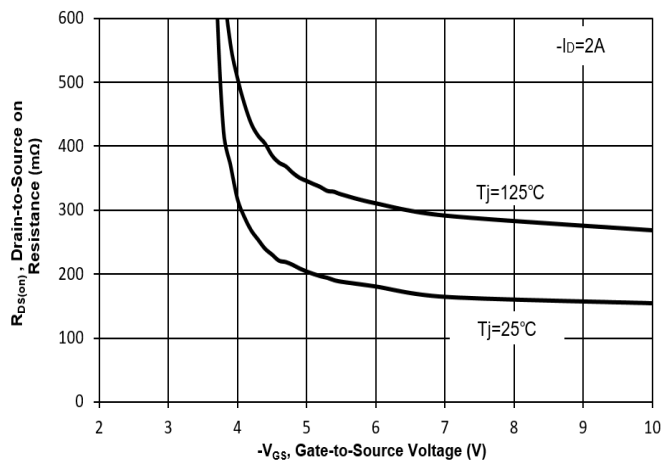


Fig. 5 on-Resistance vs. TJ

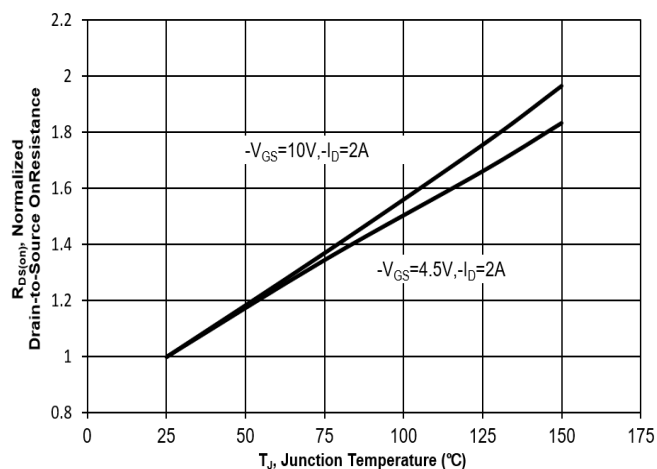
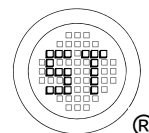
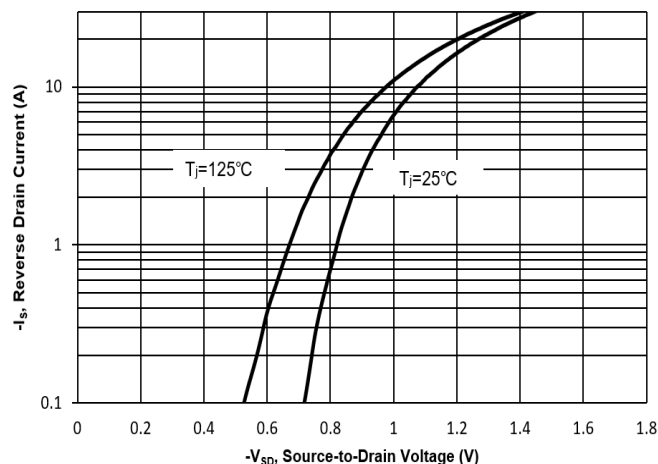


Fig. 6 Typical Body-Diode Forward Characteristics



Electrical Characteristics Curves

Fig. 7 Typical Junction Capacitance

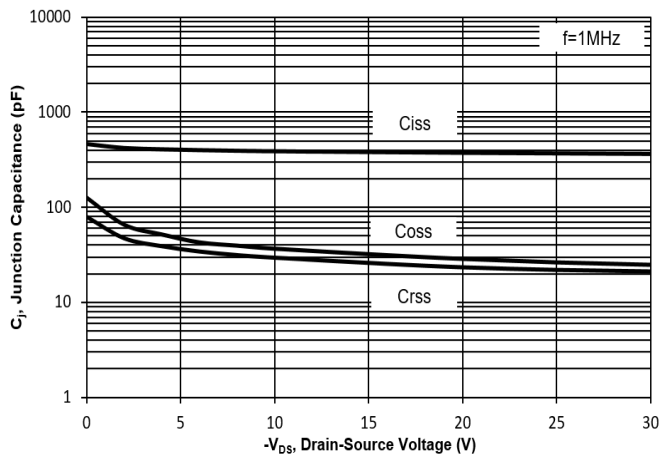


Fig. 8 Drain-Source Leakage Current vs. T_j

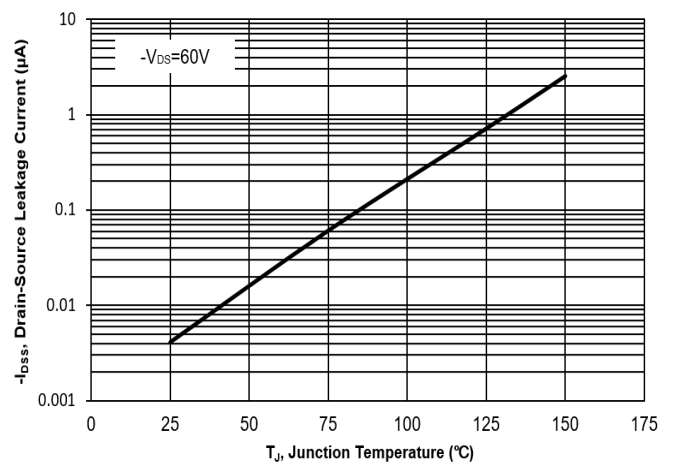


Fig. 9 $V_{(BR)DSS}$ vs. Junction Temperature

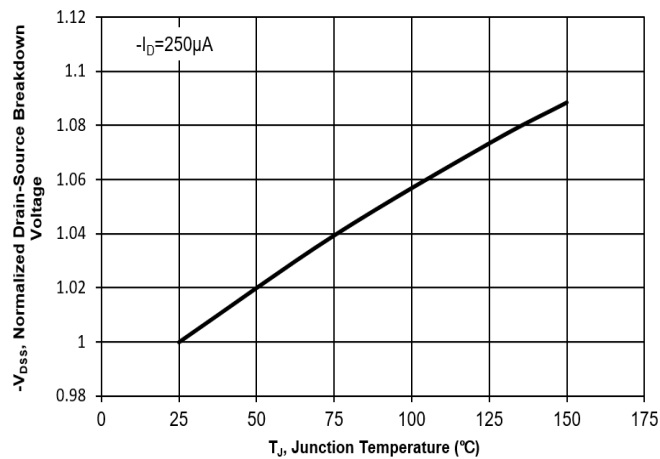


Fig. 10 Gate Threshold Variation vs. T_j

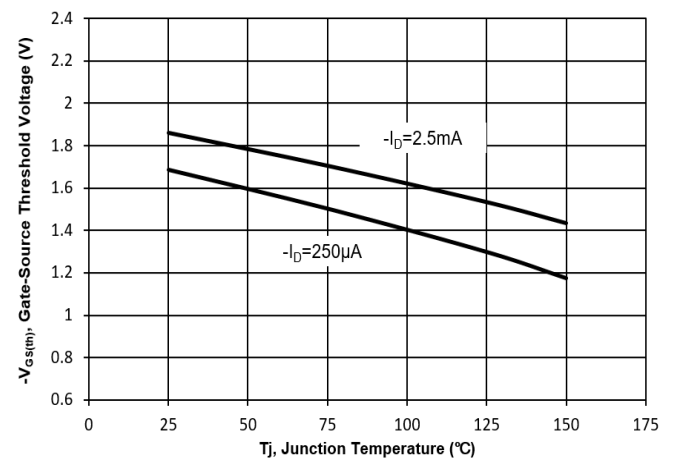
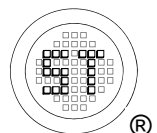
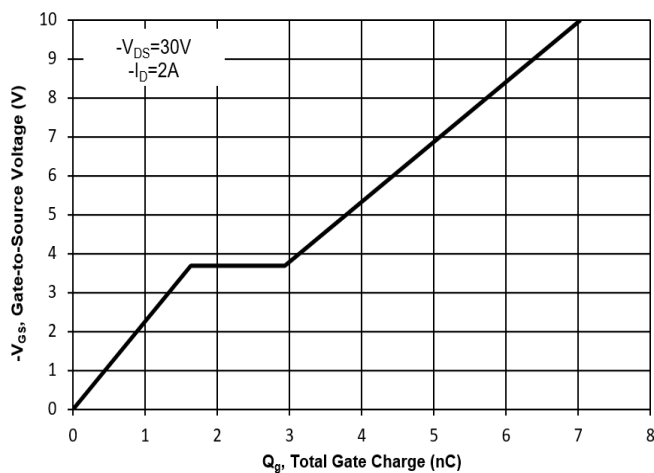


Fig. 11 Gate Charge



Test Circuits

Fig.1-1 Switching times test circuit

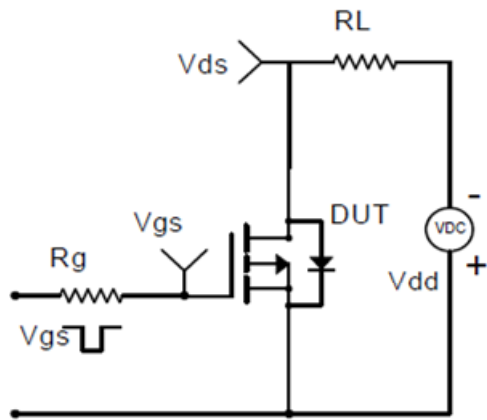


Fig.1-2 Switching Waveform

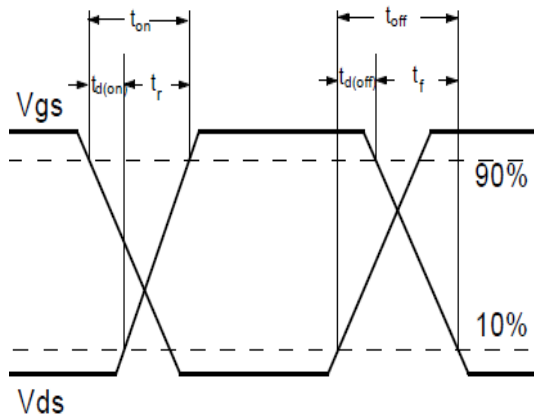


Fig.2-1 Gate charge test circuit

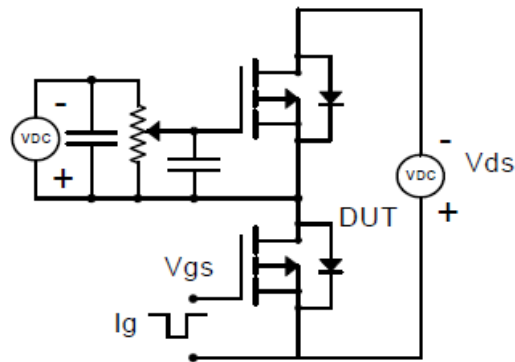
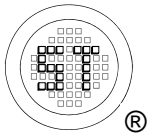
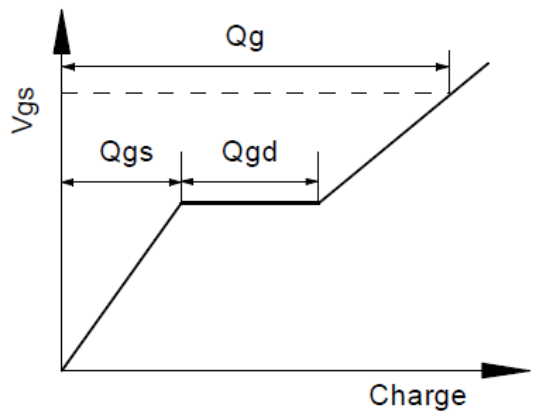


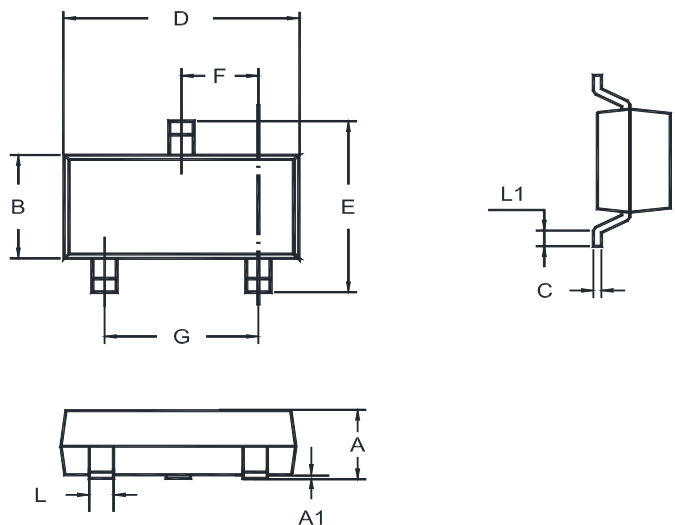
Fig.2-2 Gate charge waveform



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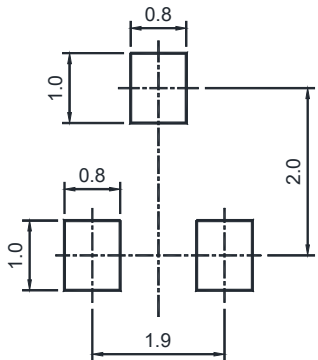
Package Outline (Dimensions in mm)

SOT-23



Unit	A	A1	B	C	D	E	F	G	L	L1
mm	1.20 0.89	0.100 0.013	1.40 1.20	0.19 0.08	3.04 2.80	2.6 2.2	1.02 0.89	2.04 1.78	0.51 0.37	0.2 MIN

Recommended Soldering Footprint



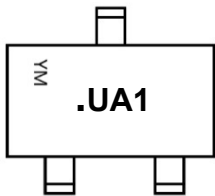
Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

Marking information

- " UA1 " = Part No.
- " • " = HAF (Halogen and Antimony Free)
- " YM " = Date Code Marking
- " Y " = Year
- " M " = Month

Font type: Arial



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